Please amend the claims as follows:

1 through 20: Canceled.

21. (Added by previous amendment): A method for stabilizing power consumption in a digital logic circuit, the method comprising:

providing a source signal;

XORing the source signal and a scrambling random signal to generate a scrambled signal;

transmitting the scrambled signal through the digital logic circuit;

passing the scrambling random signal around the digital logic circuit; and

XORing the scrambled signal and a descrambling random signal logically identical to the scrambling random signal.

22. (Added by previous amendment): The method of claim 21, additionally comprising storing the scrambling random signal.

23. (Added by previous amendment): The method of claim 21, additionally comprising:

generating the scrambling random signal using a first pseudo-random number generator responsive to a reset signal; and

generating the descrambling random signal using a second pseudo-random number generator responsive to the reset signal.

24. (Added by previous amendment): The method of claim 21, additionally comprising:

generating the scrambling random signal using a self-synchronizing scrambler responsive to the scrambled signal; and

generating the descrambling random signal using a self-synchronizing descrambler responsive to the scrambled signal.